

Course Information for EE 599: Accelerated Computing using FPGAs, Spring 2020

This class requires EE 457 and CS 570 as background courses. Also, familiarity with material covered in EE 451 is desirable.

Several students have contacted Prof. Prasanna with an intent to take this course while not having taken all the background courses at USC.

1. If you joined the program recently and have not taken the courses, you may want to consider taking this course later. This course may be offered again in Spring 2021.
2. The course grade is based mainly on a large project which will involve developing a parallel algorithm for a specific problem chosen by you, designing an application specific architecture, mapping your idea to FPGA, using the design tools and demonstrating an IP core, presenting your work and writing a report. There will be significant programming effort. You should plan carefully if you are going to be taking 2 other courses adding up to additional 8 units of load.
3. You may also enroll in the background courses at the same time as you take EE 599. This may help.
4. There will be a background assessment quiz within the first two weeks of the semester. This will be based on materials covered in CS 570, EE 457 and EE 451. Demonstrating satisfactory performance in the quiz is required to continue in the course.
5. If you are considering EE-677 as a screening course for the PhD screening exam in Computer Engineering, then the material covered in this course will be useful.
6. At this time, the ECE Student Services advisors will only grant d-clearance to students who met both prerequisites.
7. Students moved to the myViterbi Waitlist (who have Not taken CSCI-570) must attend the first class to take the assessment. If "pass," then ECE Student Services would provide d-clearance to the student upon the professor's approval.

Please email Prof. Prasanna if you need additional information.